# AP20 Rec's FST/FIO 29 JUN 2006

## **Application Data Sheet**

#### **Application Information**

**Application Type::** 

Regular

Subject Matter::

Utility

Suggested Group Art Unit::

N/A

CD-ROM or CD-R?::

None

Sequence submission?::

None

Computer Readable Form (CRF)?::

No

Title::

BIPOLAR TRANSISTOR, SEMICONDUCTOR

APPARATUS HAVING THE BIPOLAR

TRANSISTOR, AND METHODS FOR

MANUFACTURING THEM

Attorney Docket Number::

SON-3173

Request for Early Publication?::

No

Request for Non-Publication?::

No

Small Entity?::

No

Petition included?::

No

Secrecy Order in Parent Appl.?::

No

## **Applicant Information**

Applicant Authority Type::

Inventor

Primary Citizenship Country::

Japan

Status::

**Full Capacity** 

Given Name::

Masaaki

Family Name::

Bairo

City of Residence::

Kagoshima

Country of Residence::

Japan

Street of mailing address::

c/o Sony Corporation

7-35, Kitashinagawa 6-chome, Shinagawa-ku

City of mailing address::

Tokyo

Country of mailing address::

Japan

Postal or Zip Code of mailing address::

141-0001

# **Correspondence Information**

Correspondence Customer Number::

23353

## **Representative Information**

Representative Customer Number::

23353

## **Domestic Priority Information**

Application::	Continuity Type::	Parent Application::	Parent Filing Date::
This Application	National Stage of	PCT/2004/018939	12/17/04

# **Foreign Priority Information**

Country::	Application number::	Filing Date::	Priority Claimed::
Japan	2004-004186	01/09/04	Yes

# **Assignee Information**

Assignee name::

**Sony Corporation** 

Street of mailing address::

7-35 Kitashinagawa 6-Chome

Shinagawa-Ku

City of mailing address::

Tokyo

Country of mailing address::

Japan

Postal or Zip Code of mailing address::

141-0001